

Listing of the Claims

Please cancel claims 1-10 prior to examination.

Claims 1-10 (Cancelled)

11. (Original) An array panel for a transfective liquid crystal display device, comprising:
 - a substrate;
 - a thin film transistor on the substrate;
 - a first passivation layer covering the thin film transistor, the first passivation layer having a first transmissive hole and an inclined portion surrounding the first transmissive hole;
 - a reflector on the first passivation layer, the reflector completely covering the inclined portion and having a second transmissive hole corresponding to the first transmissive hole; and
 - a transparent electrode over the reflector, the transparent electrode contacting the thin film transistor.
12. (Original) The array panel according to claim 11, wherein the inclined portion slopes with an incline angle with respect to the substrate, such that light reflected at the reflector corresponding to the inclined portion is substantially reflected out of a viewing range.
13. (Original) The array panel according to claim 12, wherein the incline angle is within a range of about 20 to 110 degrees.
14. (Original) The array panel according to claim 13, wherein the incline angle is within a range of about 42 to 70 degrees.
15. (Original) The array panel according to claim 11, wherein the first passivation layer includes one of a benzocyclobutene (BCB) and a photosensitive acrylic resin.
16. (Original) The array panel according to claim 11, further comprising a second passivation layer disposed between the transparent electrode and the reflector.
17. (Original) The array panel according to claim 16, wherein the second passivation layer is transparent.

18. (Original) The array panel according to claim 16, wherein the second passivation layer includes silicon nitride.
19. (Original) The array panel according to claim 11, further comprising a gate line and a data line, the gate line and the data line crossing each other and being electrically connected to the thin film transistor.
20. (Original) The array panel according to claim 11, further comprising a gate insulator between the substrate and the first passivation layer.